

EMP Protection Units

PCM Data Lines to 2 MBit/s

Series/Type: B84320Z0010H034

Date: January 2004

© EPCOS AG 2015. Reproduction, publication and dissemination of this publication, enclosures hereto and the information contained therein without EPCOS' prior express consent is prohibited.

EPCOS AG is a TDK Group Company.



EMP protection units

B84320Z0010H034

PCM data lines to 2 MBit/s

General

The EMP protection unit is sequenced, i.e. to use simultaneously the benefits of inert-gas-filled surge arresters (extremely high surge capability) and of varistors (fast response). They are isolated by a series inductor.

The arrester is housed in a plug-in socket, so it can be removed and tested without detaching the lines.

Note on voltage figures:

The maximum voltage on the filter output depends primarily on the rise time until the arrester responds. For this reason the maximum voltage on the filter output is stated in the following table as a function of the rising edge dv/dt of the pulse.

Technical data

Rated voltage	V_R	10	V	
Rated frequency	f _R	0 2	Mbit/s	Pass bandwidth at Z _∟
Rated current	I _R	100	mA	Referred to +40 °C ambient
				temperature
Number of lines		10		Pairs
Line impedance	Z _L	124	Ω	
Maximum DC resistance	R_{max}	<2.2	Ω	Per line
Permissible ambient temperature	T _A	-25/+40	°C	
Climatic category		25/085/56		-25 °C/+85 °C/56 days damp
(EN 60068-1)				heat test
Approx. weight		300	g	
Nominal DC spark-over voltage	V_{sdcN}	<800	V	
Nominal surge current		5	kA	Line/line
(8/20 μs)		10	kA	Pair/case
Suppression condition	•	$I < I_R$		

Maximum voltage on filter output:

At rising edge	Unsymmetrical	Symmetrical
$dv/dt = 0.1 \text{ kV/}\mu\text{s}$	ŷ ≤60 V	ŷ ≤8 V
dv/dt = 1 kV/μs	ŷ ≤90 V	ŷ ≤15 V
$dv/dt = 1 kV/ns^{1)}$	ŷ ≤70 V	ŷ ≤40 V

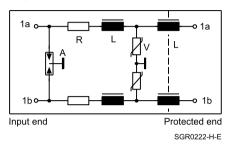
¹⁾ Typical test pulse: rise time 10 ns, time to half value 1500 ns, charge voltage min. 50 kV, source impedance 90 $\,\Omega$



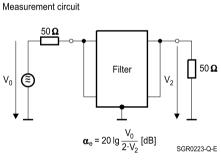
PCM data lines to 2 MBit/s

Circuit diagram

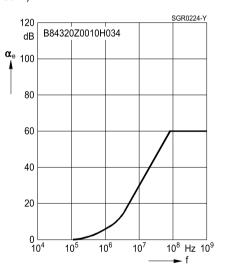
(only one of 10 pairs shown)



Insertion loss α_e per pair (typical values at Z = 50 Ω)



Asymmetrical measurement circuit to MIL-STD-220A



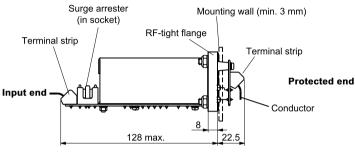


PCM data lines to 2 MBit/s

FCW data lilles to 2 Mibit/s

Dimensional drawing

Side view

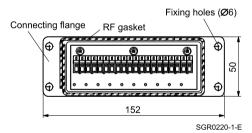


SGR0219-X-E

Terminal strip:

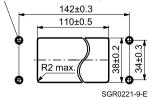
Terminals with cage strain system, suitable for conductors $0.08 \dots 2.5 \text{ mm}^2$

Front view of protected end



Installation section and attachment

Welded bolt M5x16 min. (recommended)





Important notes

The following applies to all products named in this publication:

- 1. Some parts of this publication contain statements about the suitability of our products for certain areas of application. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application. As a rule we are either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether a product with the properties described in the product specification is suitable for use in a particular customer application.
- 2. We also point out that in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
- 3. The warnings, cautions and product-specific notes must be observed.
- 4. In order to satisfy certain technical requirements, some of the products described in this publication may contain substances subject to restrictions in certain jurisdictions (e.g. because they are classed as hazardous). Useful information on this will be found in our Material Data Sheets on the Internet (www.tdk-electronics.tdk.com/material). Should you have any more detailed questions, please contact our sales offices.
- 5. We constantly strive to improve our products. Consequently, the products described in this publication may change from time to time. The same is true of the corresponding product specifications. Please check therefore to what extent product descriptions and specifications contained in this publication are still applicable before or when you place an order.
 - We also **reserve the right to discontinue production and delivery of products**. Consequently, we cannot guarantee that all products named in this publication will always be available. The aforementioned does not apply in the case of individual agreements deviating from the foregoing for customer-specific products.
- 6. Unless otherwise agreed in individual contracts, all orders are subject to our General Terms and Conditions of Supply.
- 7. Our manufacturing sites serving the automotive business apply the IATF 16949 standard. The IATF certifications confirm our compliance with requirements regarding the quality management system in the automotive industry. Referring to customer requirements and customer specific requirements ("CSR") TDK always has and will continue to have the policy of respecting individual agreements. Even if IATF 16949 may appear to support the acceptance of unilateral requirements, we hereby like to emphasize that only requirements mutually agreed upon can and will be implemented in our Quality Management System. For clarification purposes we like to point out that obligations from IATF 16949 shall only become legally binding if individually agreed upon.
- 8. The trade names EPCOS, CeraCharge, CeraDiode, CeraLink, CeraPad, CeraPlas, CSMP, CTVS, DeltaCap, DigiSiMic, ExoCore, FilterCap, FormFit, LeaXield, MiniBlue, MiniCell, MKD, MKK, MotorCap, PCC, PhaseCap, PhaseCube, PhaseMod, PhiCap, PowerHap, PQSine, PQvar, SIFERRIT, SIFI, SIKOREL, SilverCap, SIMDAD, SiMic, SIMID, SineFormer, SIOV, ThermoFuse, WindCap are trademarks registered or pending in Europe and in other countries. Further information will be found on the Internet at www.tdk-electronics.tdk.com/trademarks.