Metal Oxide Varistor

G-series varistor

Series/Type:	**G14 series**
Ordering code:	**B72214G*K101**
Date:	2025-04-07
Version:	b

Applications

- Power supplies
- Chargers
- Surge protection devices
- White goods
- Communications

Design

- Round varistor in series with arrester, leaded
- Coating: epoxy, flame-retardant according to UL 94 V-0
- Terminals: tinned copper wire, metal compound wire

Features

- Hybrid design
- Low leakage

General technical data

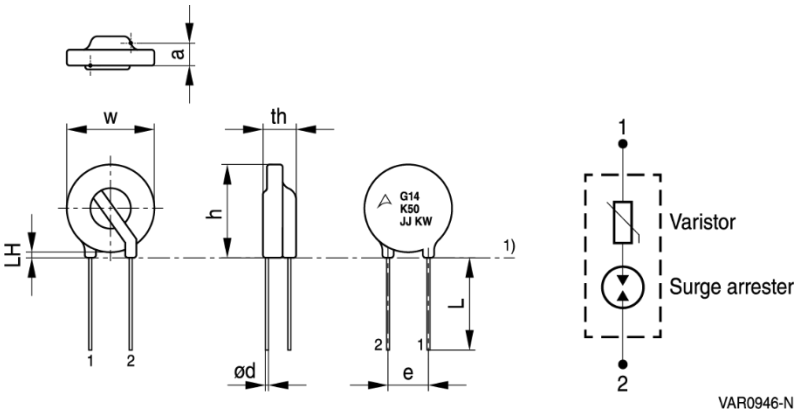
Climatic category to IEC 60068-1	40 / 105 / 56
Operating temperature	–40 … +105 °C
Storage temperature	–40 … +105 °C
Coating material	UL94-V0
Application altitude	< 2000 m

Nomenclature

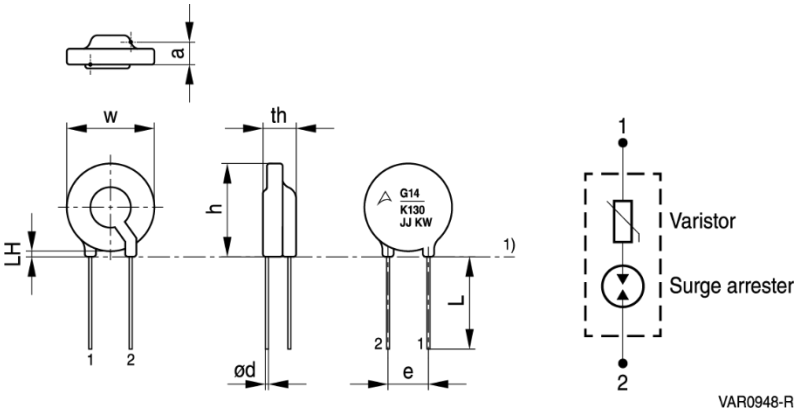
G	=	Disk type with gas tube
14	=	Rated disk diameter (mm)
K	=	Tolerance of V_V at 1 mA: ±10%
50…680	=	Max. operating AC voltage
E2	=	Energy absorption characteristics, AdvanceD series
K4	=	2-pin version

Dimensional drawing in mm

G14K…K4



G14K…E2K4



1) Seating plane in accordance with IEC 60717

Type	h_{max} (mm)	w_{max} (mm)	L_{min} (mm)	LH_{max} (mm)	Ød ±0.05 (mm)	e ±1 (mm)	a (typical) (mm)	th_{max} (mm)
G14K50K4	22	17	25	4	0.8	7.5	5.0	9.0
G14K65K4	22	17	25	4	0.8	7.5	5.0	9.0
G14K95K4	22	17	25	4	0.8	7.5	4.1	8.0
G14K115K4	22	17	25	4	0.8	7.5	4.3	8.5
G14K130E2K4	22	17	25	4	0.8	7.5	4.0	8.5
G14K140E2K4	22	17	25	4	0.8	7.5	4.0	8.5
G14K150E2K4	22	17	25	4	0.8	7.5	4.0	8.5
G14K175E2K4	22	17	25	4	0.8	7.5	4.2	8.5
G14K210E2K4	22	17	25	4	0.8	7.5	4.4	9.0
G14K230E2K4	22	17	25	4	0.8	7.5	4.4	9.0
G14K250E2K4	22	17	25	4	0.8	7.5	4.6	9.0
G14K275E2K4	22	17	25	4	0.8	7.5	4.6	9.0
G14K300E2K4	22	17	25	4	0.8	7.5	5.0	9.5
G14K320E2K4	22	17	25	4	0.8	7.5	5.0	9.5
G14K350E2K4	22	17	25	4	0.8	7.5	7.0	11.5
G14K385E2K4	22	17	25	4	0.8	7.5	7.3	11.5
G14K420E2K4	22	17	25	4	0.8	7.5	7.5	12.0
G14K460E2K4	22	17	25	4	0.8	7.5	8.0	12.5
G14K510E2K4	22	17	25	4	0.8	7.5	8.3	12.5
G14K550E2K4	22	17	25	4	0.8	7.5	8.7	13.0
G14K625E2K4	22	17	25	4	0.8	7.5	9.0	13.5
G14K680E2K4	22	17	25	4	0.8	7.5	9.4	14.0

Electrical data

Type	Ordering code[2)]	TA = 105 °C		
		Max. operating voltage	MOV voltage	DC spark-over voltage of GDT
		V_{RMS}	1 mA	100 V/s
SIOV		V	V	V
G14K50K4	B72214G0500K101*	50	68 (61 ~ 75)	300 (240 ... 360)
G14K65K4	B72214G0650K101*	65	68 (61 ~ 75)	300 (240 ... 360)
G14K95K4	B72214G0950K101*	95	120 (108 ~ 132)	470 (376 ... 564)
G14K115K4	B72214G0111K101*	115	120 (108 ~ 132)	600 (480 ... 720)
G14K130E2K4	B72214G2131K101*	130	150 (135 ~ 165)	600 (480 ... 720)
G14K140E2K4	B72214G2141K101*	140	150 (135 ~ 165)	600 (480 ... 720)
G14K150E2K4	B72214G2151K101*	150	180 (162 ~ 198)	600 (480 ... 720)
G14K175E2K4	B72214G2171K101*	175	205 (184 ~ 226)	600 (480 ... 720)
G14K210E2K4	B72214G2211K101*	210	240 (216 ~ 264)	600 (480 ... 720)
G14K230E2K4	B72214G2231K101*	230	270 (243 ~ 297)	1000 (800 ... 1200)
G14K250E2K4	B72214G2251K101*	250	270 (243 ~ 297)	1000 (800 ... 1200)
G14K275E2K4	B72214G2271K101*	275	330 (297 ~ 363)	1000 (800 ... 1200)
G14K300E2K4	B72214G2301K101*	300	330 (297 ~ 363)	1000 (800 ... 1200)
G14K320E2K4	B72214G2321K101*	320	360 (324 ~ 396)	1000 (800 ... 1200)
G14K350E2K4	B72214G2351K101*	350	390 (351 ~ 429)	1200 (960 ... 1440)
G14K385E2K4	B72214G2381K101*	385	470 (423 ~ 517)	1200 (960 ... 1440)
G14K420E2K4	B72214G2421K101*	420	510 (459 ~ 561)	1600 (1280 ... 1920)
G14K460E2K4	B72214G2461K101*	460	560 (504 ~ 616)	1600 (1280 ... 1920)
G14K510E2K4	B72214G2511K101*	510	620 (558 ~ 682)	2000 (1600 ... 2400)
G14K550E2K4	B72214G2551K101*	550	680 (612 ~ 748)	2000 (1600 ... 2400)
G14K625E2K4	B72214G2621K101*	625	750 (675 ~ 825)	2000 (1600 ... 2400)
G14K680E2K4	B72214G2681K101*	680	820 (738 ~ 902)	2000 (1600 ... 2400)

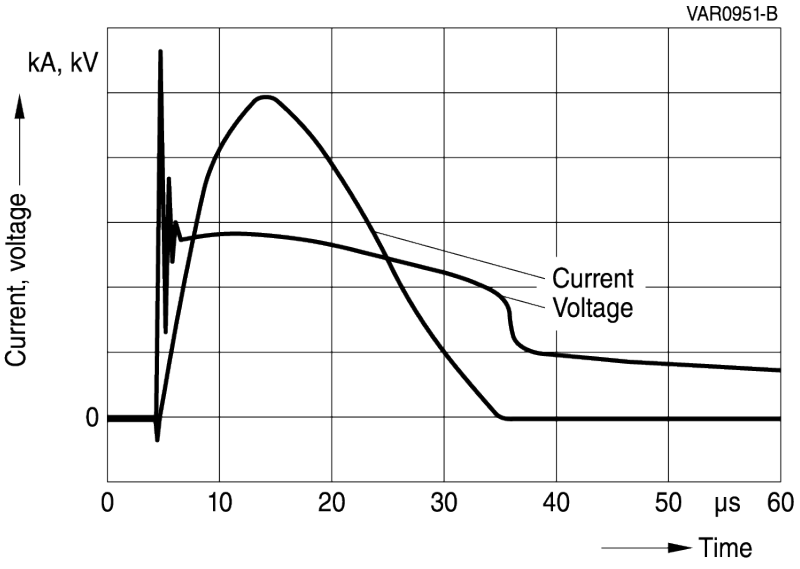
* suffix
2) Example: suffix -V87 stands for CCS wire for leads

Electrical data

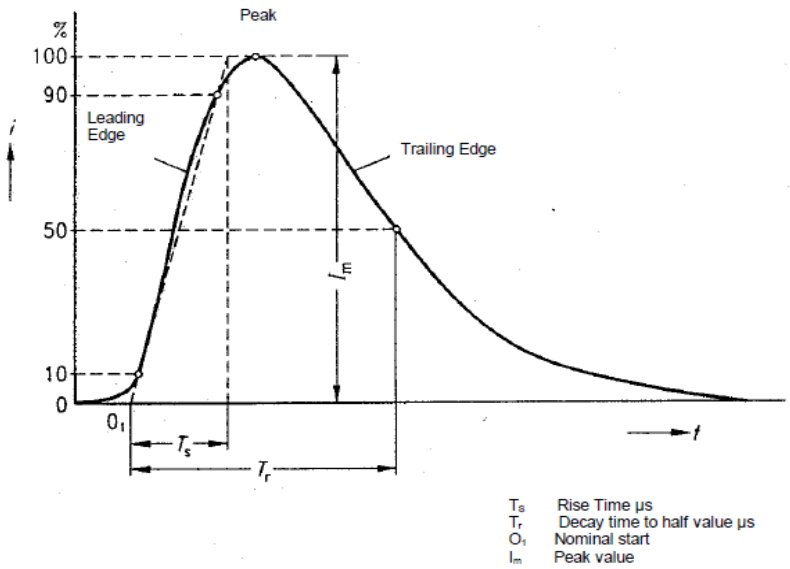
Type	Ordering code[2]	TA = 25 °C					
		I_n	I_{max}	Front of wave spark-over voltage (typical)	Clamping votage of MOV (Max)	W_{max}	P_{max}
		8/20 µs[3]	8/20 µs	at 1.2/50 µs, 6 kV	at 3 kA	2 ms	
SIOV		kA	kA	V	V	J	W
G14K50K4	B72214G0500K101*	0.5	1	1300	530	13	0.2
G14K65K4	B72214G0650K101*	0.5	1	1300	530	13	0.2
G14K95K4	B72214G0950K101*	3	4.5	1500	395	25	0.6
G14K115K4	B72214G0111K101*	3	4.5	1700	415	30	0.6
G14K130E2K4	B72214G2131K101*	3	6	1700	485	75	0.6
G14K140E2K4	B72214G2141K101*	3	6	1700	485	75	0.6
G14K150E2K4	B72214G2151K101*	3	6	1700	550	75	0.6
G14K175E2K4	B72214G2171K101*	3	6	1700	585	60	0.6
G14K210E2K4	B72214G2211K101*	3	6	1700	655	70	0.6
G14K230E2K4	B72214G2231K101*	3	6	2200	735	80	0.6
G14K250E2K4	B72214G2251K101*	3	6	2200	755	80	0.6
G14K275E2K4	B72214G2271K101*	3	6	2200	865	95	0.6
G14K300E2K4	B72214G2301K101*	3	6	2200	865	95	0.6
G14K320E2K4	B72214G2321K101*	3	6	2200	935	105	0.6
G14K350E2K4	B72214G2351K101*	3	6	2000	1045	115	0.6
G14K385E2K4	B72214G2381K101*	3	6	2000	1155	130	0.6
G14K420E2K4	B72214G2421K101*	3	6	2500	1250	140	0.6
G14K460E2K4	B72214G2461K101*	3	6	2500	1365	165	0.6
G14K510E2K4	B72214G2511K101*	3	6	2900	1630	180	0.6
G14K550E2K4	B72214G2551K101*	3	6	2900	1755	190	0.6
G14K625E2K4	B72214G2621K101*	3	6	2900	1895	200	0.6
G14K680E2K4	B72214G2681K101*	3	6	2900	2065	200	0.6

* suffix
[2] Example: suffix -V87 stands for CCS wire for leads
[3] Note: Nominal discharge current In according to UL1449.

Typical oscilloscope waveform diagram at 1.2/50 µs, 6 kV



Reliability data electrical

Characteristics	Test methods/description	Specifications
Clamping voltage	The maximum voltage between two terminals with the specified standard impulse current (8/20 µs) illustrated below applied. 	To meet the specified value. It tested between pin1 and pin2, but spark-overvoltage of surge arrester is not included.
Surge current derating, 8/20 µs	10 surge currents (8/20 µs), unipolar, interval 30 s, amplitude corresponding to derating curve for 10 impulses at 20 µs	No visibile damage
Electric strength	IEC 61051-1, test 4.9.2 Metal balls method, 2500 V_{RMS}, 60 s The varistor is placed in a container holding 1.6 ±0.2 mm diameter metal balls such that only the terminations of the varistor are protruding. The specified voltage shall be applied between both terminals of the specimen connected together and the electrode inserted between the metal balls.	No breakdown

Reliability data mechanical

Characteristics	Test methods/description	Specifications
Solderability	IEC 60068-2-20, test Ta, method 1 with modified conditions for lead-free solder alloys: 245 °C, 3 s: After dipping the terminals to a depth of approximately 3 mm from the body in a soldering bath of 245 °C for 3 s, the terminals shall be visually examined.	The inspection shall be carried out under adequate light with normal eyesight or with the assistance of a magnifier capable of giving a magnification of 4 to 10 times. The dipped surface shall be covered with a smooth and bright solder coating with no more than small amounts of scattered imperfections such as pinholes or unwetted or wetted areas. These imperfections shall not be concentrated in one area.
Resistance to soldering heat	IEC 60068-2-20, test Tb, method 1A, 260 °C, 10 s: Each lead shall be dipped into a solder bath having a temperature of 260 ±5 °C to a point 2.0 to 2.5 mm from the body of the unit, be held there for 10 ±1 s and then be stored at room temperature and normal humidity for 1 to 2 h. The part shall be visually examined.	No visible damage

Reliability data environmental

Characteristics	Test methods/description	Specifications
Endurance at upper category temperature	IEC61051-2-2, 1000 h at UCT After having continuously applied the maximum allowable voltage at UCT ±2 °C for 1000 h, the specimen shall be stored at room temperature and normal humidity for 1 to 2 h.	No visible damage
Damp heat, steady state	IEC 60068-2-78, test Ca The specimen shall be subjected to 40 ±2 °C, 90 to 95% RH for 56 days without load / with 10% of the maximum continuous DC operating voltage V_{DC}. Then stored at room temperature and normal humidity for 1 to 2 h. Thereafter, insulation resistance R_{ins} shall be measured at V = 500 V (insulated varistors only).	No visible damage R_{ins} ≥ 100 MΩ

Note:

UCT = Upper category temperature

LCT = Lower category temperature

R_{ins} = Insulation resistance

Cautions and warnings

General

1. TDK Electronics metal oxide varistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with TDK Electronics during the design-in-phase.
2. Ensure suitability of SIOVs through reliability testing during the design-in phase. SIOVs should be evaluated taking into consideration worst-case conditions.
3. For applications of SIOVs in line-to ground circuits based on various international and local standards there are restrictions existing or additional safety measures required.

Storage

After shipment from TDK Electronics the SIOV type series should be soldered within the following time periods:

SIOV-S,-Q,L(S),-SNF,-ICL,-B,-E	24 months
SIOV-ETFV,-T,-SMD,-MT-EM,-NT,-G,-GNF	12 months
SIOV-D	6 months
SIOV-D(Cu)	3 months

The parts are to be left in the original packing to prevent oxidized terminals which can cause soldering problems.

Storage temperature:	–25 to 45 °C
Max. relative humidity (without condensation):	< 75% annual average, < 95% on max. 30 days per annum

Handling

1. SIOVs must not be dropped.
2. Components must not be touched with bare hands. Gloves are recommended.
3. Avoid contamination of the surface of SIOV electrodes during handling, be careful of the sharp edge of SIOV electrodes.

Soldering (where applicable)

1. Use rosin-type flux or non-activated flux.
2. Insufficient preheating may cause ceramic cracks.
3. Rapid cooling by dipping in solvent is not recommended.
4. Complete removal of flux is recommended.
5. Temperature of all preheat stages and the solder bath must be strictly controlled especially for T series (T14 and T20).

Mounting

1. Potting, sealing or adhesive compounds can produce chemical reactions in the SIOV ceramic that will degrade the component’s electrical characteristics.
2. Overloading SIOVs may result in ruptured packages and expulsion of hot materials. For this reason, SIOVs should be physically shielded from adjacent components.

Operation

1. Use SIOVs only within the specified temperature operating range
2. Use SIOVs only within the specified voltage and current ranges.
3. Environmental conditions must not harm the SIOVs. Use SIOVs only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas, etc.), corrosive agents, humid or salty conditions. Contact with any liquids and solvents should be prevented.

This listing does not claim to be complete, but merely reflects the experience of TDK Electronics.

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